Figure 9

Index counter/ repeating

| 0000 | 0001 | 0010 | 0011 | 0100 | 0101 | 0110 | 0111 | 1000 | 1001 | 1010 | 1011 | 1100 | 1101 | 1110 | 1111 |

recovered clock
(carrier cycles)

recovered
data pulse

If index=1 and
data pulse

Y

(implied data = 0000)

If index=2 and
data pulse

Y

(implied data = 0001)
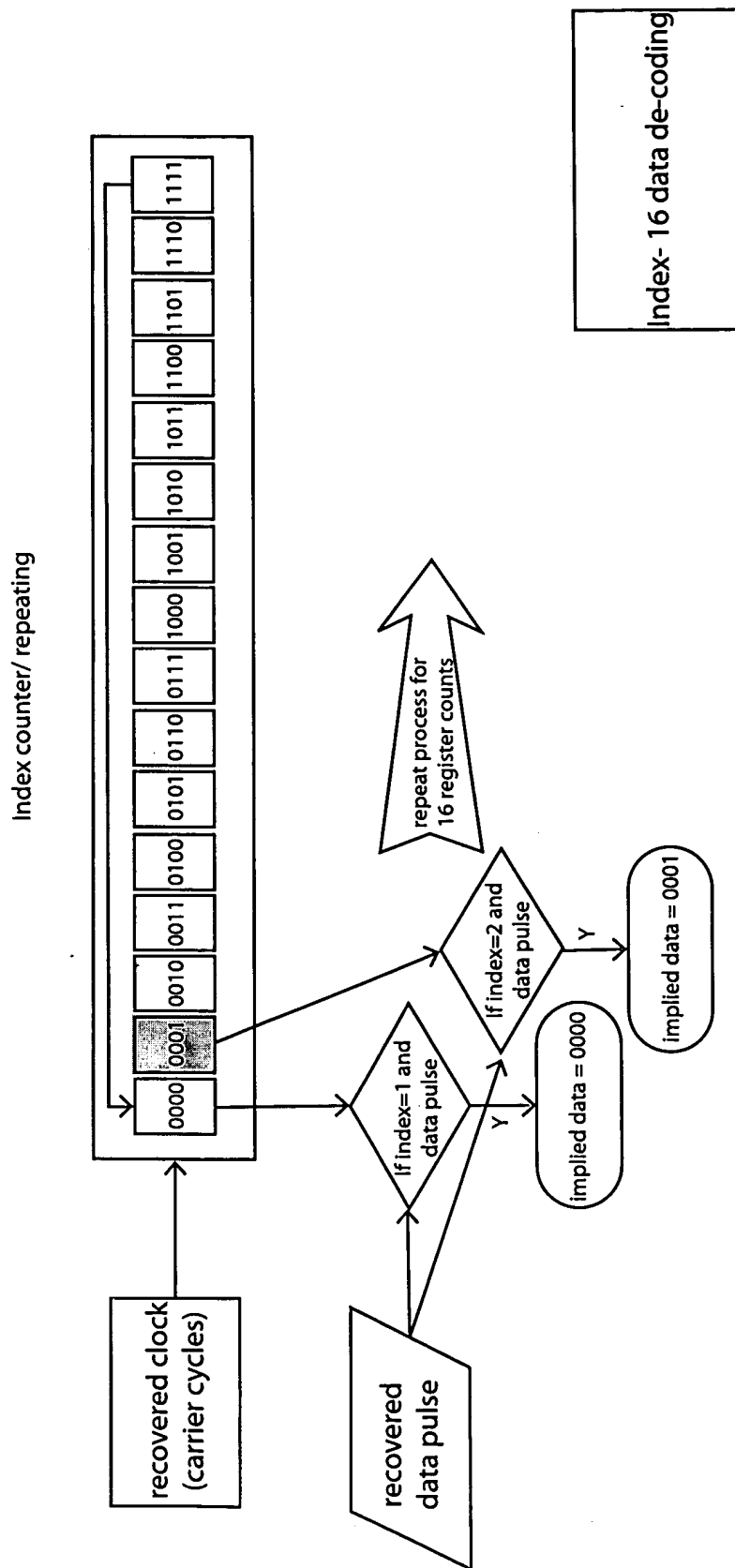
repeat process for
16 register counts

Index- 16 data de-coding

Figure 10

encoded serial data frame register